

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	0	creat\$5 netlist object target hardware architectre field programmable gate array dummy unused simulat\$7 functional hardware description language	US-PGPUB	AND	ON	2007/10/24 11:15
L2	0	creat\$5 netlist object target hardware architectre	US-PGPUB	AND	ON	2007/10/24 11:15
L5	87	creat\$5 netlist object modif\$5 dummy hardware architecture	US-PGPUB	AND	ON	2007/10/24 11:16
L8	53	creat\$5 netlist object modif\$5 dummy language hardware	US-PGPUB	AND	ON	2007/10/24 11:17
L9	58	creat\$5 netlist object modif\$5 dummy language circuit design	US-PGPUB	AND	ON	2007/10/24 11:18
L10	53	creat\$5 netlist object modif\$5 dummy language circuit design hardware	US-PGPUB	AND	ON	2007/10/24 11:18
L11	6352	((703/13-14) or (716/9-10, 12-17)).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/10/24 11:21
L12	13	11 and 9	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/24 11:21